Preferred Device

Switch-mode Power Rectifier

Ultrafast "E" Series with High Reverse Energy Capability

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

Features

- 20 mjoules Avalanche Energy Guaranteed
- Excellent Protection Against Voltage Transients in Switching Inductive Load Circuits
- Ultrafast 75 Nanosecond Recovery Time
- 175°C Operating Junction Temperature
- Low Forward Voltage
- Low Leakage Current
- High Temperature Glass Passivated Junction
- These are Pb-Free Devices*

Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 0.4 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max for 10 Seconds
- Polarity: Cathode Indicated by Polarity Band

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|--|--------------------------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 1000 | ٧ |
| Average Rectified Forward Current (Note 1) | I _{F(AV)} | 2.0 @ T _A = 35°C | Α |
| Non-Repetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz) | I _{FSM} | 35 | Α |
| Operating Junction Temperature and Storage Temperature Range | T _J , T _{stg} | −65 to +175 | °C |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Value | Unit |
|--|-----------------|----------|------|
| Maximum Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | (Note 3) | °C/W |

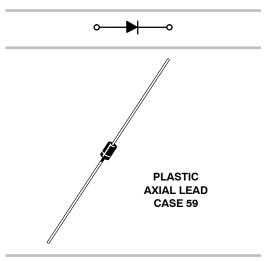
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

http://onsemi.com

ULTRAFAST RECTIFIER2.0 AMPERES, 1000 VOLTS



MARKING DIAGRAM



A = Assembly Location

/ = Year

WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

| Device Package | | Shipping [†] |
|----------------|--------------|-----------------------|
| MUR2100E | Axial Lead** | 1000 Units/Bag |
| MUR2100EG | Axial Lead** | 1000 Units/Bag |
| MUR2100ERL | Axial Lead** | 5000/Tape & Reel |
| MUR2100ERLG | Axial Lead** | 5000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

^{1.} Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2.0%.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

^{**}This package is inherently Pb-Free.

ELECTRICAL CHARACTERISTICS

| Characteristic | Symbol | Value | Unit |
|---|-------------------|--------------|------|
| Maximum Instantaneous Forward Voltage (Note 2) $ \begin{array}{l} (I_F=2.0 \text{ A, T}_J=150^{\circ}\text{C}) \\ (I_F=2.0 \text{ A, T}_J=25^{\circ}\text{C}) \end{array} $ | V _F | 1.75 2.20 | V |
| Maximum Instantaneous Reverse Current (Note 2) (Rated dc Voltage, T_J = 100°C) (Rated dc Voltage, T_J = 25°C) | İR | 600 10 | μΑ |
| Maximum Reverse Recovery Time $ \begin{aligned} (I_F = 1.0 \text{ A, di/dt} = 50 \text{ A/}\mu\text{s}) \\ (I_F = 0.5 \text{ A, }I_R = 1.0 \text{ A, }I_{REC} = 0.25 \text{ A}) \end{aligned} $ | t _{rr} | 100 75 | ns |
| Maximum Forward Recovery Time (I _F = 1.0 A, di/dt = 100 A/μs, I _{REC} to 1.0 V) | t _{fr} | 75 | ns |
| Controlled Avalanche Energy (See Test Circuit in Figure 6) | W _{AVAL} | 10 | mJ |

^{2.} Pulse Test: Pulse Width = 300 $\mu s,$ Duty Cycle \leq 2.0%.

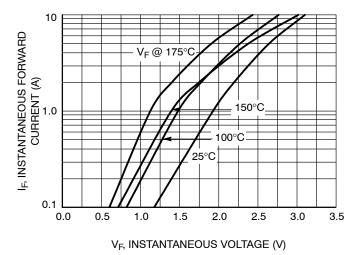


Figure 1. Maximum Forward Voltage

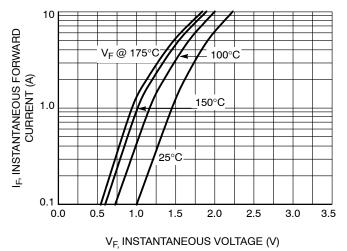


Figure 2. Typical Forward Voltage

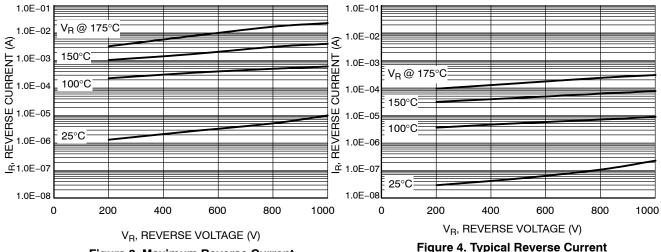


Figure 3. Maximum Reverse Current

Figure 4. Typical Reverse Current

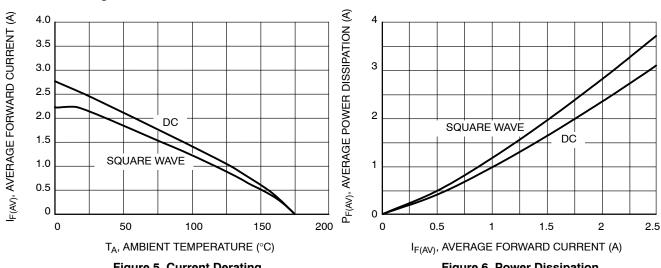


Figure 5. Current Derating

Figure 6. Power Dissipation

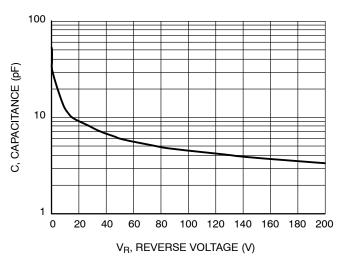


Figure 7. Typical Capacitance

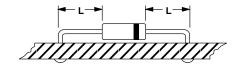
NOTE 3. – AMBIENT MOUNTING DATA

Data shown for thermal resistance, junction–to–ambient $(R_{\theta JA})$ for the mountings shown is to be used as typical guideline values for preliminary engineering or in case the tie point temperature cannot be measured.

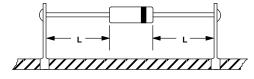
TYPICAL VALUES FOR $\textbf{R}_{\theta \text{JA}}$ IN STILL AIR

| Mounting | | Lead Length, L | | | |
|----------|-----------------|----------------|-----|-----|-------|
| Method | | 1/8 | 1/4 | 1/2 | Units |
| 1 | | 52 | 65 | 72 | °C/W |
| 2 | $R_{\theta JA}$ | 67 | 80 | 87 | °C/W |
| 3 | | | 50 | | °C/W |

MOUNTING METHOD 1

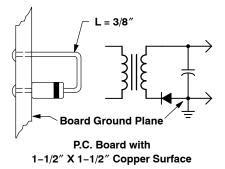


MOUNTING METHOD 2



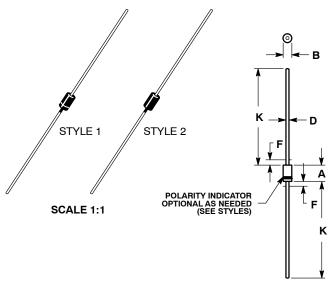
Vector Pin Mounting

MOUNTING METHOD 3



AXIAL LEAD CASE 59-10 **ISSUE U**

DATE 15 FEB 2005



STYLE 2:

NO POLARITY

STYLE 1: PIN 1. CATHODE (POLARITY BAND)

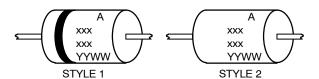
2. ANODE

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

- CONTROLLING DIMENSION: INCH.
 ALL RULES AND NOTES ASSOCIATED WITH
 JEDEC DO-41 OUTLINE SHALL APPLY
 POLARITY DENOTED BY CATHODE BAND.
 LEAD DIAMETER NOT CONTROLLED WITHIN F
 DIMENSION.

| ĺ | | INCHES | | MILLIM | ETERS | |
|---|-----|---------|-------|--------|-------|--|
| l | DIM | MIN MAX | | MIN | MAX | |
| [| Α | 0.161 | 0.205 | 4.10 | 5.20 | |
| I | В | 0.079 | | | 2.70 | |
| I | D | 0.028 | | | 0.86 | |
| I | F | 0.050 | | | 1.27 | |
| I | K | 1.000 | | 25.40 | | |

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code = Assembly Location Α

YY = Year WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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|------------------|-------------|---|-------------|
| DESCRIPTION: | AXIAL LEAD | | PAGE 1 OF 1 |

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