

REV. Status

REVISION -06/23/03

REVISION A ADDED PAGES 3-4. 07/08/03 MP F. Reliability Test;

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┪	No.	Item	Condition	Specifications		
	1	Solderbility	Temperature: 230° ± 5°C	After that the sample shall be		
			Solder time: 3 ± 0.5 seconds	covered by solder uniformly at more than 90% of circumference.		
			Solder: H60A or H63A Flux: 75% Methanol and	more than 90% of circumference.		
			25% Rosin			
f	2	Resistance to	Temperature: $260^{\circ} \pm 5^{\circ}C$	Sample shall not show any		
		Soldering heat	Solder time: 10 ± 1 seconds	unusual appearance.		
			Solder: H60A or H63A			
			Flux: 75% Methanol and			
ŀ	3	Resistance to	25% Rosin Temperature: 350° ± 10°C	Sample shall not show any		
	3	soldering heat	Solder time: 3 ± 1 seconds	unusual appearance.		
		(hand soldering)	solder time. 3 = 1 seconds	anasaar appearance.		
t	4	Thermal cycle	JIS C 0025 10 cycles	After that sample shall be replaced in		
		test	Temperature	normal ambient for 60 min., it shall not		
			-10°C 30 min	show any unusual appearance and should		
			25°C 5 min 70°C 30 min	meet the requirement of dielectric		
			70 C 30 Hilli	strength and insulation resistance no less than $10M\Omega$		
	5	Heat test	JIS C 0021	After that sample shall be replaced in		
			Temperature: 100°C	normal ambient for 60 min., it shall not		
			Time: 96 hours	show any unusual appearance and should		
				meet the requirement of dielectric		
				strength and insulation resistance no less than $10M\Omega$		
ŀ	6	Cold test	JIS C 0020	After that sample shall be replaced in		
	Ü	Cold test	Temperature: -25°C	normal ambient for 60 min., it shall not		
			Time: 96 hours	show any unusual appearance and should		
				meet the requirement of dielectric		
				strength and insulation resistance no less than $10M\Omega$		
-	7	Humidity Test	JIS C 0022	After that sample shall be replaced in		
	,	Training Tost	Temperature: 40°C	normal ambient for 60 min., it shall not		
			Humidity: 90~95%	show any unusual appearance and should		
			Time: 96 hours	meet the requirement of dielectric		
				strength and insulation resistance no less		
-	8	Vibration test	JIS C 0040	than $10M\Omega$ After that sample shall be replaced in		
	5	, ioiution tost	Frequency: 10~55Hz	normal ambient for 60 min., it shall not		
			Amplitude (total excursion)	show any unusual appearance and should		
			1.5mm	meet the requirement of dielectric		
			Transverse time: 5 min. Direction Time: XYZ each 50	strength and insulation resistance no less		
			than 10MΩ			
+	9	Terminal	min. JIS C 0051.2.5	No breakage of magnet wire, etc.		
strength 5N 10 seconds						
Ī)BAW/IN	NG CONTROL NO. RE	WODEL DESCRIPTION	N MODEL SPECIFICATION		
	Р	-A1-12972	TELECOMMUNICATION SURFAC	CE MOUNT		
-		$\TC\A1129722.DWG$	TILLIDI CARRARITALI AF			
	SUBJE	ITS OF THIS DRAWING A CT TO CHANGE WITHO NOTICE	43352 BUSINESS PARK DRIVE, TEMECULA, C	A. 92590-6624 DIM: N/A SCIENT/A SU 2 05 4		
F			(909) 699-1270 FAX 90967	DISCLOSED HEREIN ARE DRODDIETARY ITEMS TO WHICH TANIBA		

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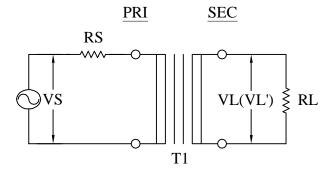
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REVISION A ADDED PAGES 3-4. 07/08/03 MP

G. Test Methods;

1. Frequency Response:

FR=20 LOG
$$\left| \frac{VL}{VL'} \right|$$
 (dB)



VS: Constant input voltage (V)

VL: Output voltage at any other frequency (V)

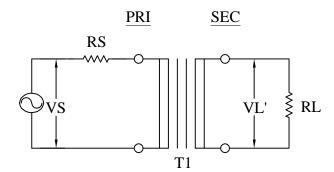
VL: Output voltage at reference frequency, 1KHz (V)

RS: Primary source impedance (Ω) RL: Secondary load impedance (Ω)

T1: Transformer under test

2. Insertion Loss:

IL=10 LOG
$$\left| \frac{\text{VS}^2 \text{x RL}}{4 \text{ VL}^2 \text{x RS}} \right|$$
 (dB)



VS: Input voltage (V)

VL: Output voltage at frequency, 1KHz (V)

RS: Primary source impedance (Ω) RL: Secondary load impedance (Ω)

T1: Transformer under test

	DRAWING CONTROL NO. REV P-A1-12972 ACAD\TTC\A1129723.DWG A		MODEL DESCRIPTION TELECOMMUNICATION SURFACE MOUNT	MODEL SPECIFICATION TTC - 512			
ACA			DRY COUPLING TRANSFORMER				
			TAMURA CORPORATION OF AMERICA				
			43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624 (909) 699-1270 FAX 9096769482	DIM: N/A	SCL: N/A	SH: 3 OF	4

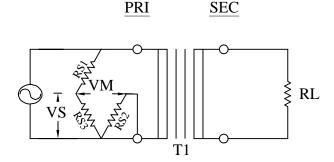
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3. Return Loss: $RL=20 LOG \left| \frac{VS}{VM} \right| (dB)$



VS: Divided input voltage (V) VM: Unbalanced voltage (V)

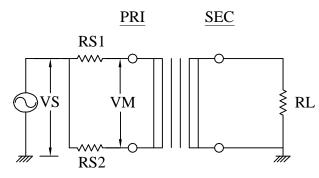
RS1, RS2, RS3: Primary source impedance (n

RL: Secondary load impedance (Ω)

T1: Transformer under test

4. Longitudinal Balance:

$$LB=20 LOG \left| \frac{VS}{VM} \right| (dB)$$



VS: Input voltage (V)

 $VM: Longitudinal\ unbalanced\ voltage\ (V)$

RS1, RS2: Half of primary source impedance (n)

RL: Secondary source impedance (Ω

T1: Transformer under test

	DRAWING CONTROL NO. P-A1-12972 ACAD\TTC\A1129724.DWG A		MODEL DESCRIPTION TELECOMMUNICATION SURFACE MOUNT	MODEL SPECIFICATION TTC - 512			
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